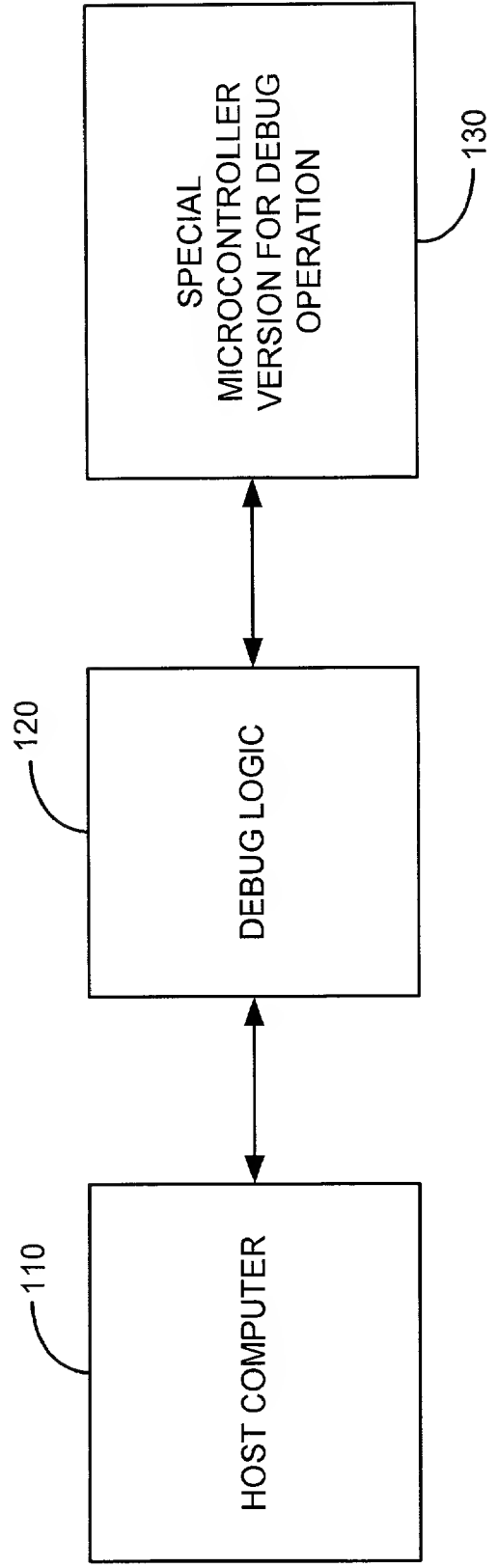


FIG. 1 is a block diagram of a system 100 for debugging a microcontroller 130. The system 100 includes a host computer 110, debug logic 120, and the microcontroller 130. The host computer 110 is connected to the debug logic 120, which is in turn connected to the microcontroller 130. The microcontroller 130 is a special microcontroller version for debug operation.



100

(PRIOR ART)

**FIG. 1**

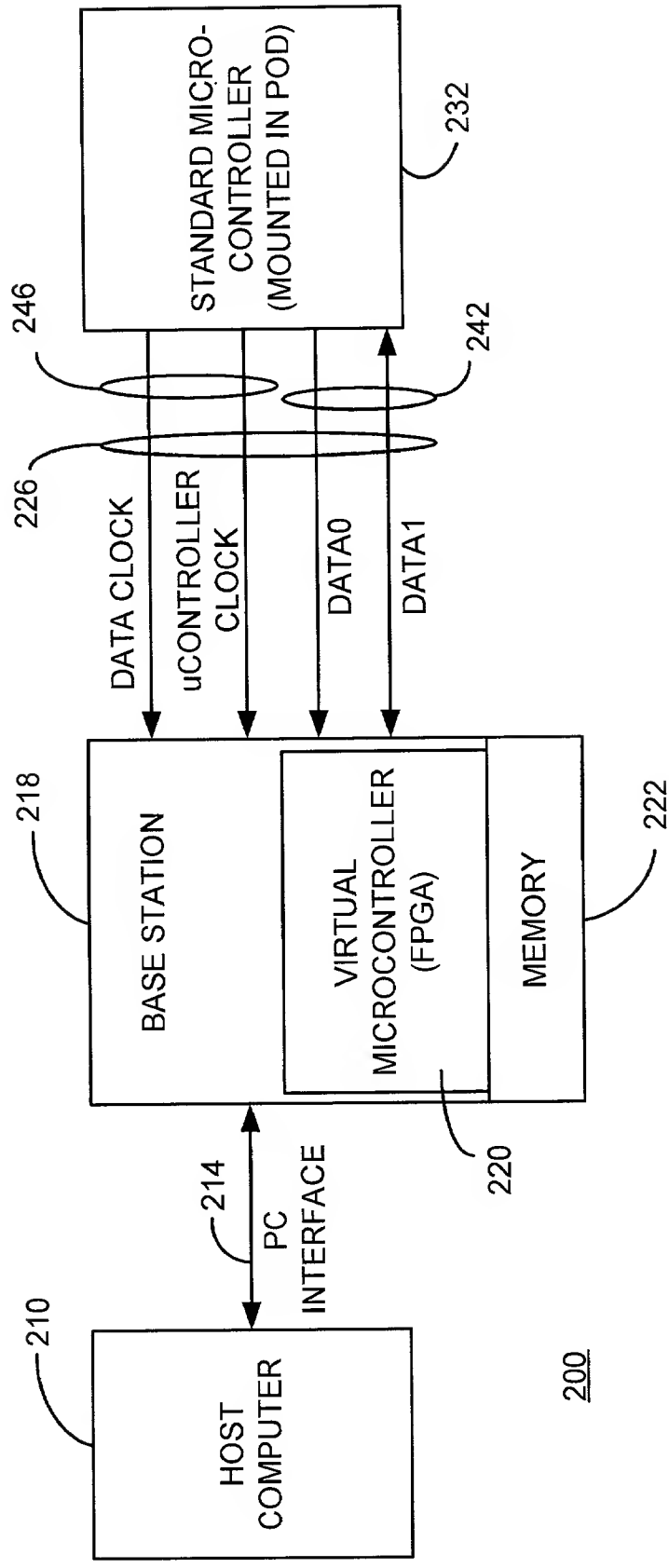
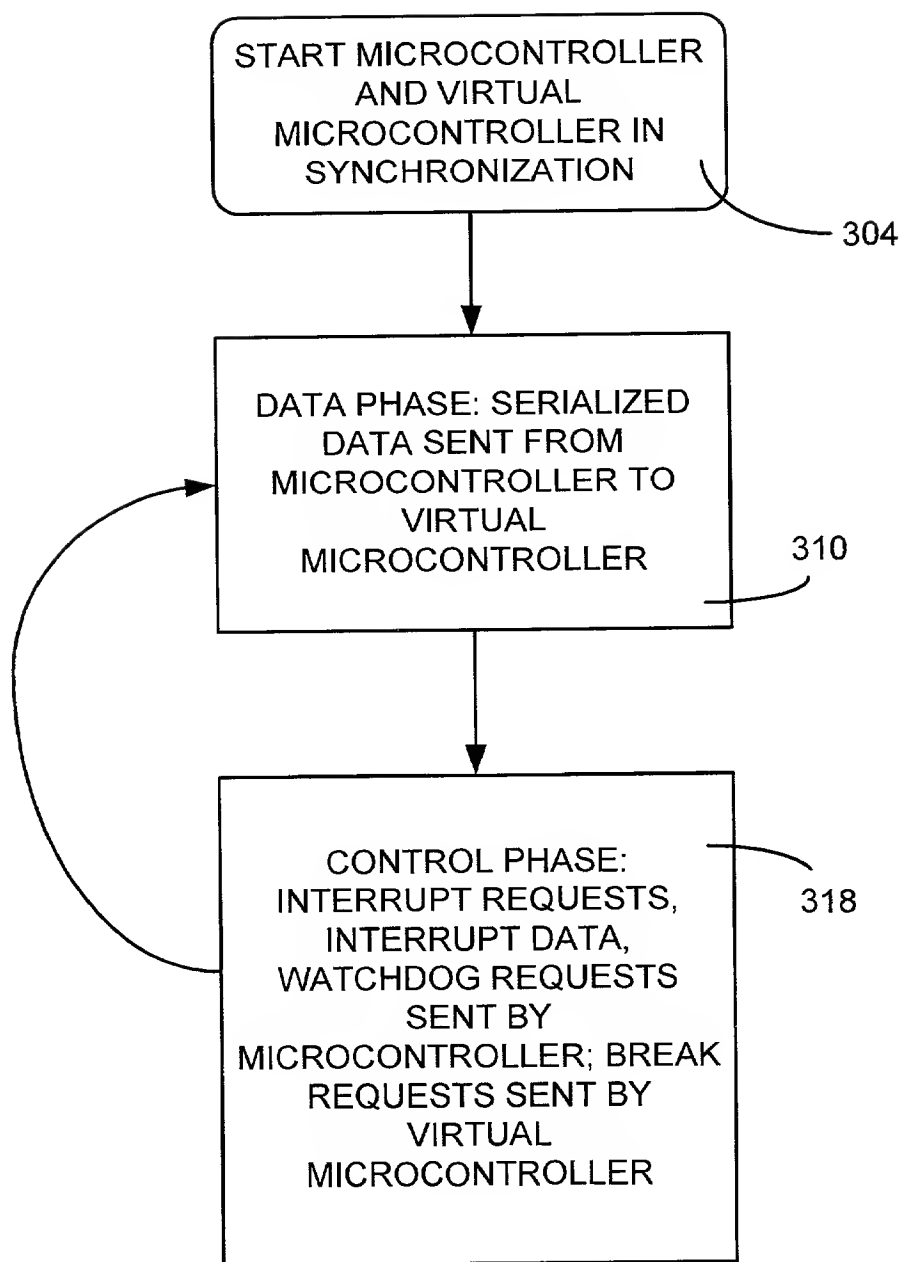
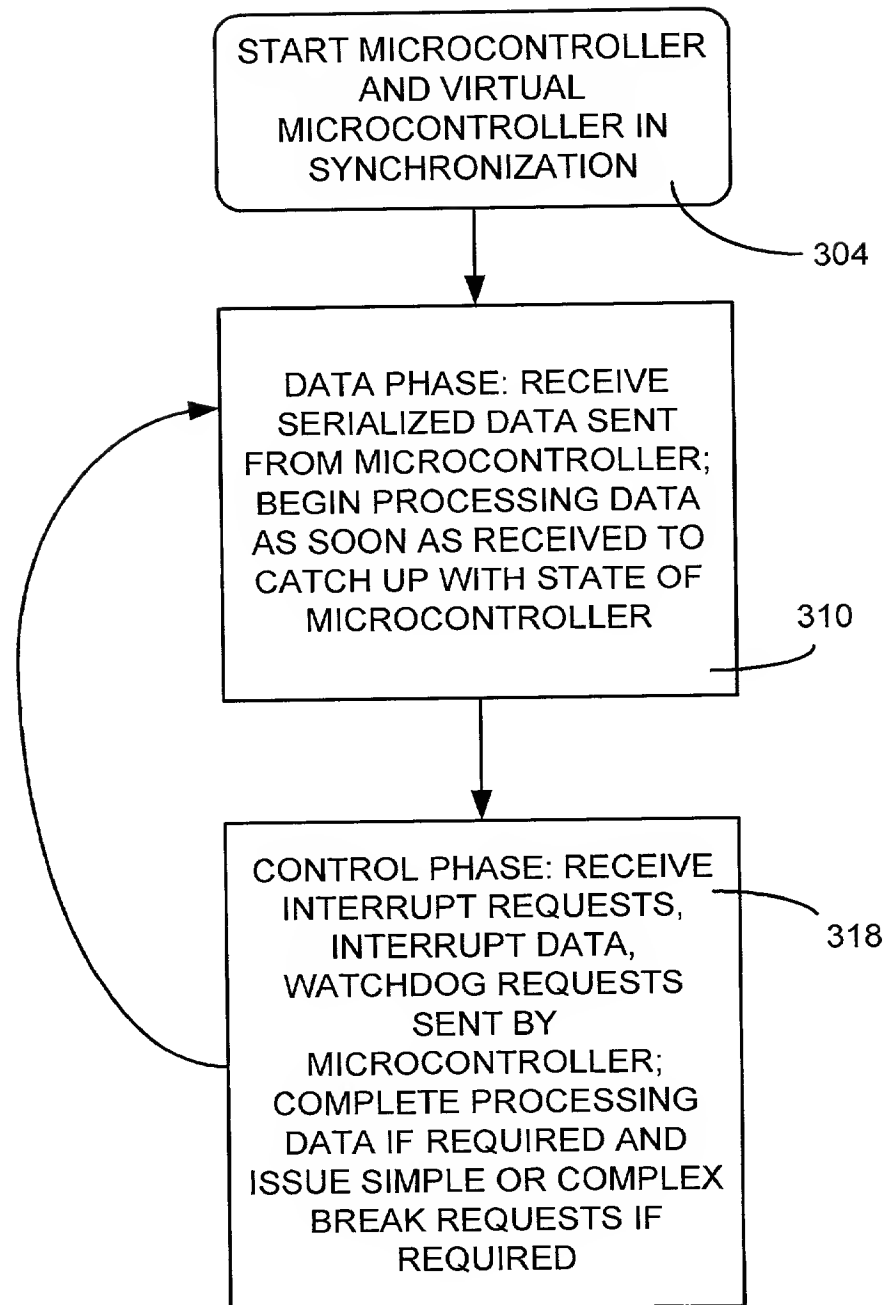


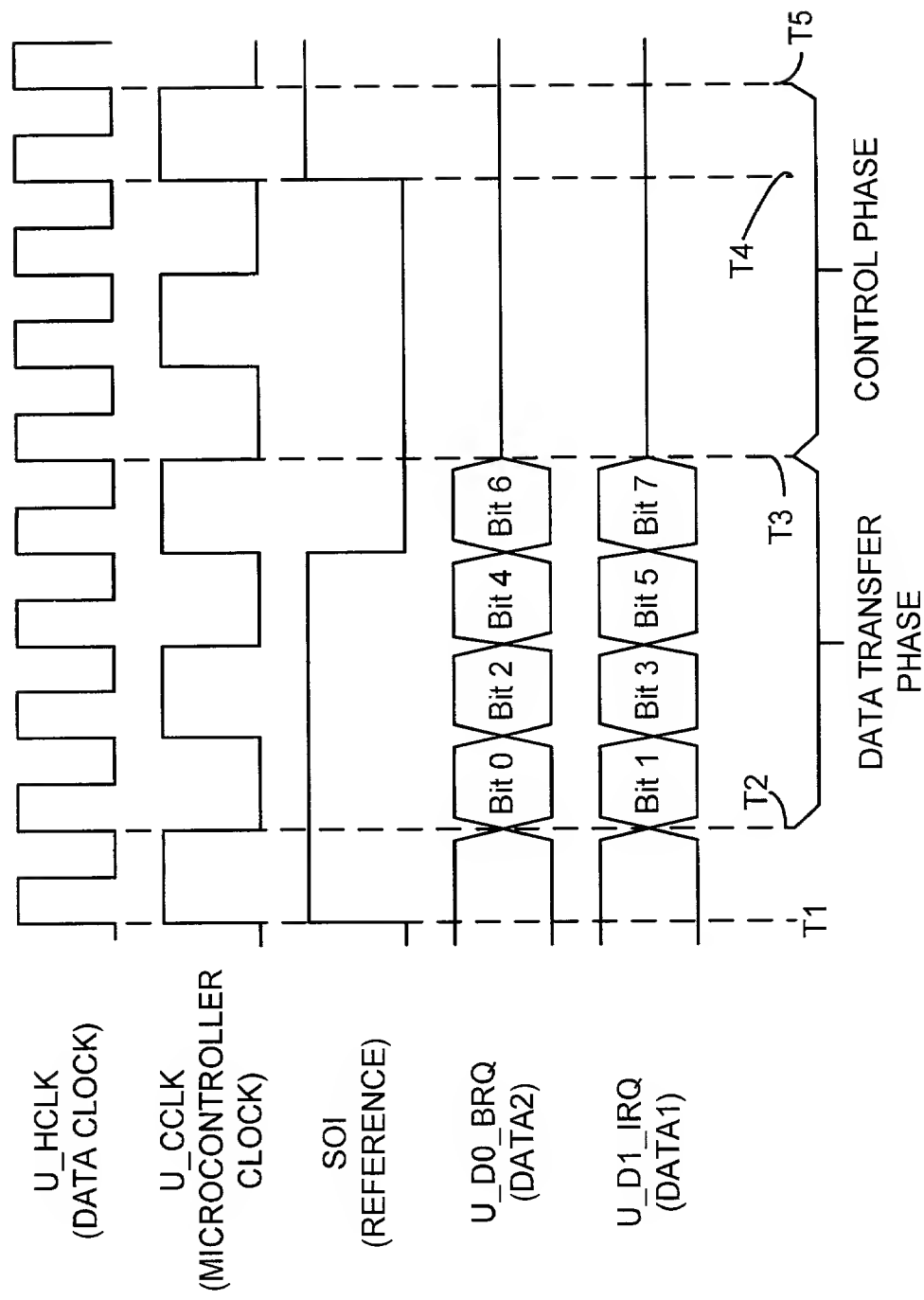
FIG. 2



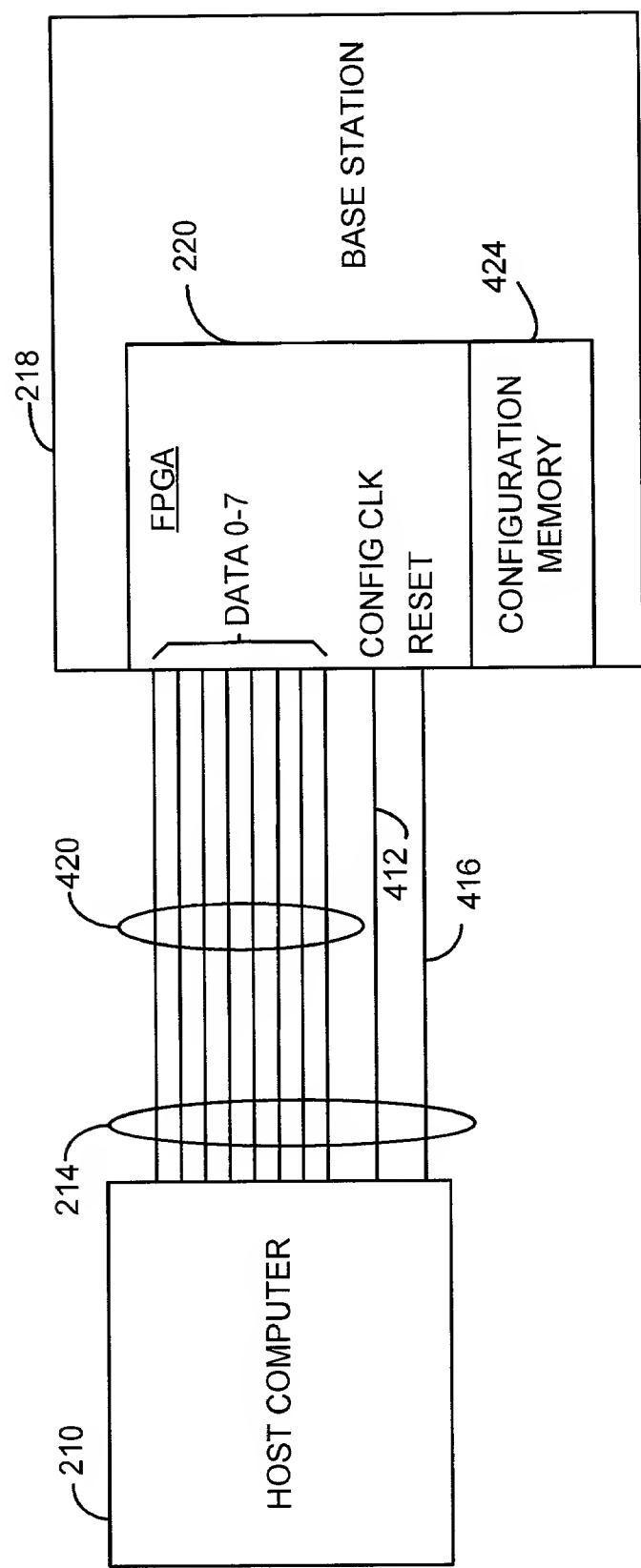
**FIG. 3**

**FIG. 4**

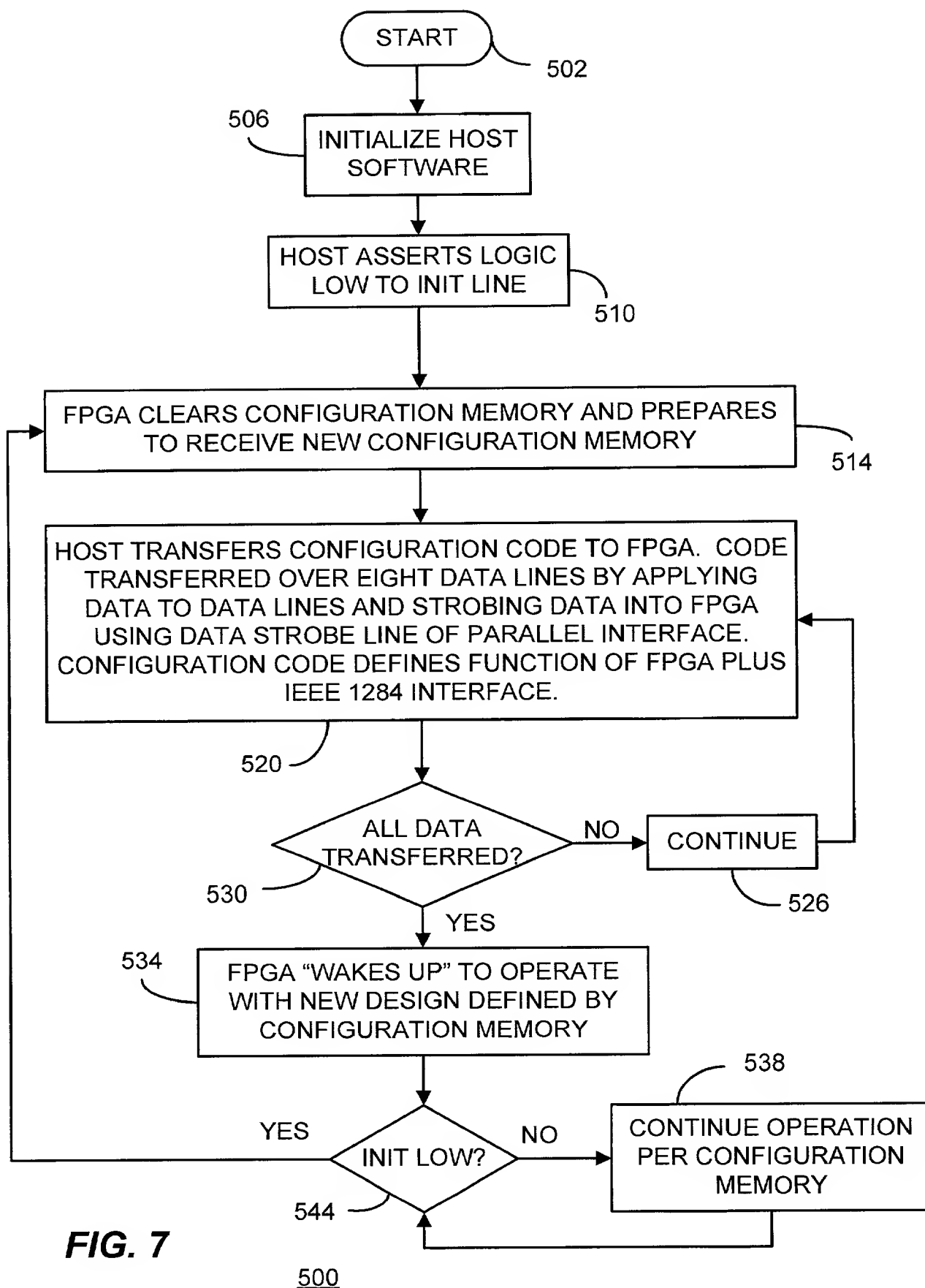


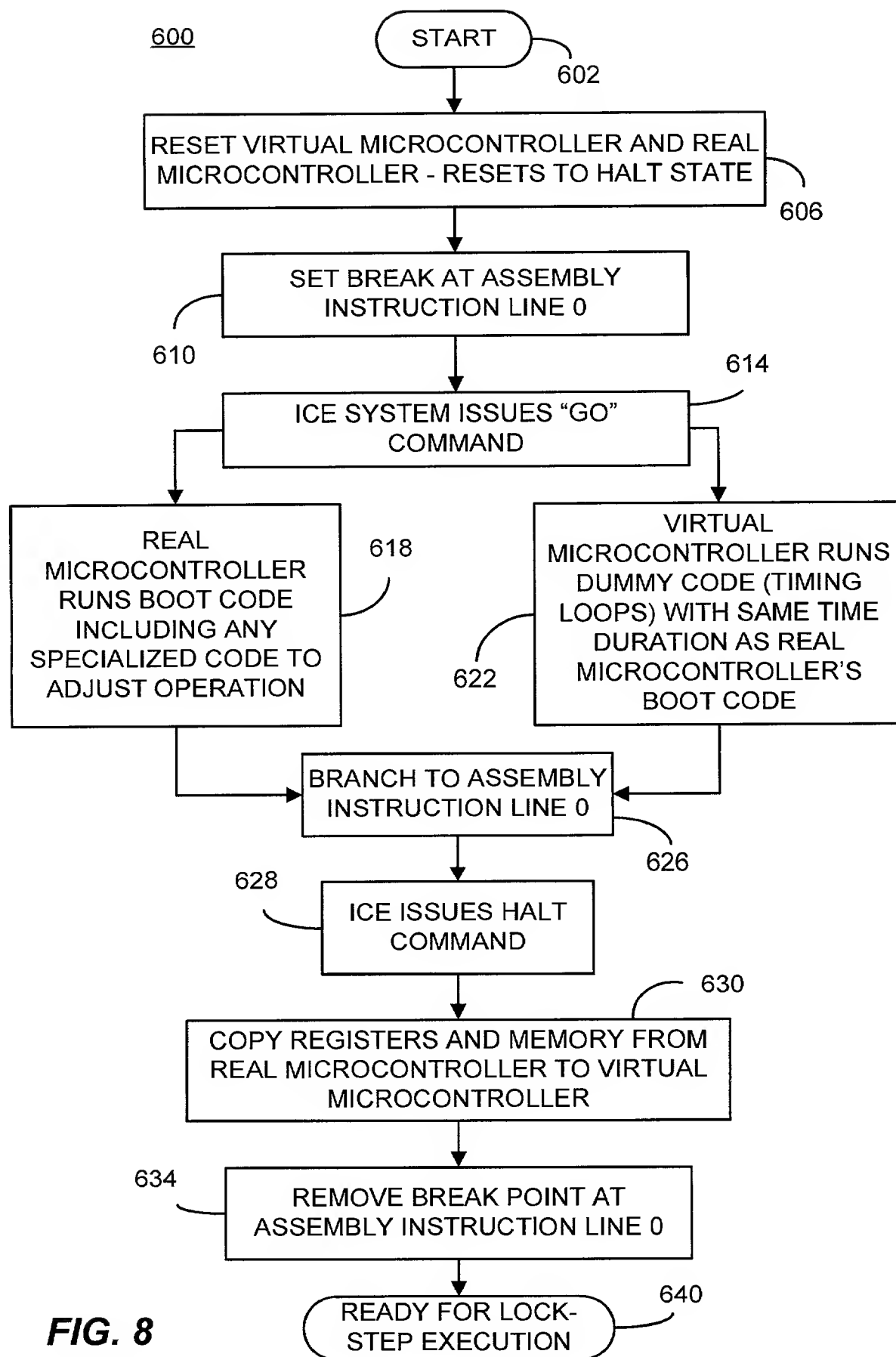


**FIG. 5**



**FIG. 6**





**FIG. 8**